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Milling a silicon nitride membrane by focused ion beam

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An ultrathin amorphous membrane, such as that made of silicon nitride (SiN) suspended on silicon substrate, is a popular platform for various applications. However, its hardness causes many difficult technical problems for patterning, especially when combined with other fabrication processes. In nanofabrication, focused ion beam (FIB) is a popular milling technique. It would be a perfect tool for perforating the SiN membrane, but the ion beam charges the membrane, induces stress, and breaks them sporadically. The authors design a metallic structure near the cutting area to neutralize the charges. It reduces stress on the membrane and enables the perforation. Commercial SiN membranes are perforated with FIB and are suspended on thin legs on silicon chip. The authors study and discuss various designs and the applicability of this approach. © 2016 American Vacuum Society. [http://dx.doi.org/10.1116/1.4963895]

I. INTRODUCTION

Thin film membranes made of amorphous materials are popular in mesoscopic physics and nanotechnology as they provide a two dimensional platform for various experiments and applications. For example, in biological research, silicon nitride membranes are being used as supporting structures in biomedical applications,1,2 as a porous mass transfer platform in cell culturing,3 as a nanosieve4 in fluid separation membranes, or as electrodes in electrochemistry.5 In physics applications, nitride membranes are used in photonic nanocrystals6 (PhC), in two-dimensional phononic crystal (PnC) structures,7 or as a window for x-ray and transmission electron imaging. Of particular interest, silicon nitride membranes are used as an isolated platform8–12 that separates cold and hot parts of a thermal machine. A refrigerator that operates at ultralow temperature made from superconducting tunnel junctions13–15 stays on the bulk Si and extends its cold fingers to the membrane. To ensure an efficient performance of the refrigerator, the membrane is perforated so that its central piece, the cold part, is suspended on thin legs. It is hard to pattern such fragile membrane and make it compatible with other fabrication steps.

A wide range of amorphous materials and composites can be suspended on a silicon wafer with thickness down to a few tens of nanometer and area up to mm2. However, because they are so inert and durable, it is tricky to pattern them into desired structures. Typically, etching of the membranes is done with highly corrosive chemicals, or performed under extreme physical conditions, such as high temperature and detrimental plasma. Such processes are harmful to the device of interest and are incompatible with other fabrication processes. Moreover, the membranes, now suspended on thin legs, are very fragile and it is nearly impossible to further manipulate them. It is important to develop a perforation method that gives a high yield and is compatible with a wide range of standard microfabrication techniques.

Alternatively, a maskless process can be employed to perforate dielectric membranes. An electron beam at a high energy16,17 as well as an helium ion beam18 have been used to punch nanopores in SiN membrane in biophysical research. Here, a high energy beam of accelerated particles sputter material from the target area. In nanofabrication, focused ion beam (FIB) using gallium is well known as a machining tool for modifying devices at micro- and nanometer scales.19–21 FIB milling is a slow process, because a small size focused beam sweeps over an area smaller than 1 mm2 at a time, but can very well be used in small batch fabrication, especially in research. The advantage of the FIB milling is that it can easily be done at any stage of the device fabrication due to its local material removal, and absence of chemicals, liquid surface tensions, and thermal stress. FIB has open substrate requirements and can pattern nanometer sized areas with precise alignment. The only requirements for FIB processing are the vacuum compatibility, a low minimum electrical conductivity of the substrate, and that the material hardness does not exceed 9 in Mohs scale. Depending on the application, redeposition of gallium during implantation and milling may affect the target quality. Also, implantation of gallium during the milling cannot be avoided and that might change the target properties such as etch rate.22 High energy gallium ions also amorphize a thin surface layer of crystalline target material on a hit,23 which is undesirable mostly for HR-TEM imaging. On an insulating surface, such as the suspended SiN membrane, the ion current charges the dielectric membrane,24 increases tension, and can break the membrane.

In this paper, we engineer a mesh of metal surrounding the perforated area to neutralize the charged membrane during milling with the FIB [Fig. 1(a)], thus effectively preventing charging. The neutralization mesh is designed so that when milling in specific order, the residual stress25 in the membrane is minimized and thus reduces the possibility of breaking them. The SiN membrane is perforated so that the
final outcome is a small center piece that is suspended on thin legs. We study in detail the perforating conditions and discuss the application of this approach. It would be also possible to remove charging by covering the whole membrane with metal, but metal on the back side of the membrane would have detrimental effect on sensitive devices on the top surface. The top side is not possible to cover with extra material layers, since it contains devices. It is also necessary to avoid increasing device fabrication time, by minimizing the amount of surface area that should be covered by electron beam lithography exposure by not exposing any unnecessary patterns in the process step. Drawing narrow patterns is a better choice than covering large areas in electron beam lithography. In our case, the mesh itself is possible to fabricate in the same lithography process steps as the functional devices on the device platform that need environmental isolation, as they both can be fabricated of the same 30 nm thick copper. Fabricating both, the mesh and devices, in the same process steps saves time, reduces cost, and keeps the whole process simpler.

II. FABRICATION

All silicon chips are diced into $5 \times 5 \text{mm}^2$ squares, with a suspended $1 \text{mm} \times 1 \text{mm} \times 100 \text{nm}$ SiN-membrane in the middle. They are either ordered from Norcada$^{26}$ or made in-house. Poly(methyl methacrylate) of 200 nm from MicroChem was spun at 2500 round per minute for 1 min, and then baked at 170°C for 5 min. Charge neutralization patterns were exposed by VISTEC EBPG 5000+ES electron beam lithography system with current 50 nA and dose 1000 $\mu$C/cm$^2$. The sample is developed in 1:3 solution of methyl isobutyl ketone and isopropanol for 1 min. It is then loaded in a Telemark 241 electron beam evaporator, and 30 nm of copper is deposited on top. Lift-off of Cu was done at temperature 80°C in mr-Rem 400 or mr-Rem 500 from Microresist for 10 min, rinsed in isopropanol, and dried with dry nitrogen air flow.

Test membranes were loaded into a FEI Company (Helios Nanolab 600) focused dual beam system and perforated with gallium ions at 30 kV. Electron micrographs are taken in situ during the perforation.

III. EXPERIMENT

An initial study of thin 100 nm SiN membranes proved that a low current of 0.92 nA on a nonpatterned membrane is sufficient to very often cause the membrane to over charge and crack. Milling with low currents does not provide sufficient protection against charging and they increase milling speeds significantly. Repeated exposure of these low current beams over the same pattern, therefore, cannot be used to
perforate the membrane successfully and within acceptable speed. On the other hand, membranes completely covered with thin layer of metal reduces cracking rate significantly. Also, it was noted that currents higher than 2.8 nA can break the membrane, even with some charge conduction present [Figs. 1(c) and 1(d)]. Based on these facts, two studies were carried out to investigate how to reduce the residual stress on the membrane, and speed up the milling, without having to cover the whole membrane in the metal layer. In the first experiment, we want to find out a suitable perforation mesh shape [Fig. 1(b)]: a comb shape [Figs. 1(e)–1(h)] or a square shape [Figs. 1(i) and 1(j)]. We change the distance between the comb shape structure and the milling area and observe when the membrane breaks. A second study focuses on cutting near a square shape [Fig. 1(j)]. Distances between the perforated region and the conducting wire were varied and statistics of membrane breakage were collected. A total of 29 sequences for parallel comb and 30 sequences for perpendicular combs were cut for the experiment. An ion beam of 2.8 nA was used in cutting and was chosen as a compromise between the cutting speed and avoiding heavy local charging. Choosing a 2.8 nA ion current was considered justified, and due to the device limitations and specifications constrained by the given device, we needed to fabricate on the device platform. These devices on the device platform are for other experiments and lie outside of the scope of this paper. By taking the electron image in situ while cutting, it is possible to observe the behavior and the buckling of the membrane and to determine if the membrane perforation was either successful, or not. The buckling itself is difficult to observe in action with SEM, but can also be confirmed by observing the failing adhesion and wrinkles appearing into deposited metal wires after heavy charging of the membrane.

For testing the efficiency of the separation distance, rectangle shaped conductive patterns were used because they performed the best compared to comb shapes. A total of 50 sequences were used where each membrane was patterned with copper rectangles of sizes 60, 70, 80, 100, and 140 μm [Fig. 2(a)]. A square milling shape of size 60 × 60 μm² [squares in Figs. 1(c)–1(j)] was milled with the FIB at the center of the each pattern, starting the milling from the smallest square. Selected design generates five different separation distances to be tested in the test membranes. The separation distance in the smallest square pattern is 0 μm, and then increases to 5, 10, 20, and 40 μm at the largest square. After milling the smallest square, and if the membrane did not break, the milling advances to next largest square. The whole sequences of different size square patterns are milled from smallest to largest using the same milling area. If the membrane did not break at the largest separation distance of 40 μm, we mark the membrane cut as successful, as it survived all five cuts with different separation distances. Each membrane had three columns of five different sized squares. This makes it possible to run at most three complete

![Fig. 2](image-url)
sequences on each membrane. If the membrane survives the first sequence, the second sequence is run on the same membrane, and if the membrane survives then the third sequence is run, until all of the 15 squares have been perforated.

After confirming the most effective conductive pattern for the application, we turn our focus on applying this knowledge into practical solution. The original problem was to isolate a thermal device from the environment by perforating the thin SiN membrane into the suspended platform. We pattern a 30 nm thick copper mesh [Fig. 3(a)] together with our thermal devices on the SiN membrane, and use this mesh as charge conductive pattern and as a guidelines for cutting. After the platform has been released from the bulk silicon, all of the copper mesh patterns and SiN residue should be removed as milling debris by then leaving no unnecessary material around [Fig. 3(c)]. The released device platform with thermal devices on it is then used for further experiments, not reported in this paper.

IV. RESULTS

To find a suitable conducting pattern for discharging, three different patterns were selected for testing, two comb shapes and a square [see Fig. 1(b)]. After cutting several membranes, we noticed that the membranes patterned with parallel comb break most often [Figs. 1(g), 1(h), and 2(b)]. Strongest charging halo was observed in this pattern type. It is possible that the spike of the comb nearest to milling area has to remove most of the charge, but cannot do it properly alone. Thicker and wider spikes might improve the perforation. Statistics for cutting the comb structure is shown in Fig. 2(b). Least charging effects were observed in the square pattern. The square pattern surrounds the whole milled area and thus effectively collects all charge applied to the area. The fact that we can perforate the membrane with a neutralization structure confirms our assumption that it is the charging from the gallium ions that induce stress and cause the membrane to break. The rectangular pattern was selected for further testing, since it appeared to be the most promising.

After selecting the pattern design, we then focus on perforating squares of $60 \times 60 \, \mu m^2$ dimension. This size is a good trade-off between cutting speed and yield. Cutting a hollow square of this size on 100 nm thick SiN membranes takes 53 s with a 2.8 nA beam current, which is a reasonable speed. By cutting a larger area, the perforation gets slightly faster, but then the breaking rate of the membranes rapidly increases due to uneven stress produced on the membrane. From the experience of processing several full size membranes, it is safe to cut a square hole of $100 \times 100 \, \mu m^2$ or smaller without any stress reduction. For the experiment, we stick to the $60 \times 60 \, \mu m^2$ square. This perforation size can be milled at any location on the membrane, without increasing considerably the probability of breaking it. The maximum hole size on the membrane without a strain stress reduction is a square of size $200 \times 200 \, \mu m^2$. In a hole size of this the charge neutralization pattern becomes irrelevant, since now it is the strain stress that starts to dominate the membrane breaking mechanism. This could be confirmed by painting the whole membrane with a 10 nm platinum layer and trying to cut a large hole in it in various locations with catastrophic consequences [Figs. 1(c) and 1(d)]. Perforating a single hole of this size or larger turns out to be not possible at all.

Out of 50 test sequences with square patterns, all survived the separation distances of 0, 5, and 10 $\mu m$ without problems. Problems start to accumulate when distance is 20 $\mu m$ or higher. Membranes that are not cracked at all are considered survived membrane. Membranes that are slightly cracked are labeled as cracked. At 20 $\mu m$ distance, six out of 50 is cracked, from which one is completely broken, thus yielding a 12% crack rate and a 98% success rate. At 40 $\mu m$, numbers get even higher, and 60% of the membranes are cracked and 52% are broken totally [Fig. 2(b)]. The charging halo on the membrane became most visible at the patterns that had 20 and 40 $\mu m$ separation distance to the Cu neutralization wire, and these were the same patterns that were involved with the most membrane breaking [Figs. 2(b) and 2(d)] as shown in the result summary chart. Apparently, a distance of 10 $\mu m$
from the conductor provides an efficient charge cancellation for square patterns and thus guarantees a successful milling of the membrane. For comb patterns, the safe distance is lower and starts from 5 μm. This is our most important result. It clearly demonstrates that the guidance of the produced charging is necessary when perforating a nonconducting film, and out of the three different tested pattern types, the rectangle works the best. Furthermore, we demonstrated that the effect is distance dependent.

Perforated membranes and membranes containing strain stress are both fragile. If membranes under strain stress have to be perforated, it is desirable to perforate them into a shape that gives up in order to reduce or relax the stress. To perforate the whole membrane into an shape that gives up, e.g., spider web, suspended bridges, zig zag, spiral, or serpentine shape, it is better to start cutting not larger than 100 μm squares at opposite sides from the edge. In order to perforate and relax our test membrane, we pattern a 30 nm thick copper mesh on the area (Fig. 3). The membrane is relaxed by cutting first the outermost layer of squares [red squares in Figs. 3(a) and 3(b)] around the edge, and then proceeding toward the membrane center with the next layer (blue squares, and then green squares). Cutting is best done evenly from opposing sides, few squares at the time, to prevent imbalanced stress build up, which could break the membrane. This way the stress load is reduced evenly, leaving the isolated device platform in the center [Fig. 3(c)]. Cutting too much at once from one side or from the center of the membrane significantly increases the risk of breaking it. This happens because the imbalance of stress between opposing edges will tear the membrane. In this experiment, 60 × 60 μm² size squares were cut around the membrane edge, no more than three squares at a time on opposing edges. A square hole of 60 × 60 μm² was considered to be small enough to have some extra safety margin compared to 100 × 100 μm² size hole, thus making sure not to cut too large holes. Once three circles of rectangles were milled around the edge, so that the device platform was surrounded by a flexible web, we cut the remaining mesh area in random order. Final stage was that all holes were connected so that an isolated island with devices was left hanging from a few narrow bridges [Fig. 3(c)].

V. DISCUSSION

Having an excellent mechanical and chemical durability, silicon nitride is the most common membrane available for research and applications in microfabrication. However, with a typical thickness less than a micron—an ultrahigh aspect ratio, this amorphous membrane is extremely fragile to external impacts. The nitride film stress phenomenon with a yield stress in the order of 1 GPa is well known to exist, especially between the silicon substrate and silicon nitride thin film.27 where a thin silicon oxide film is used between the two in device fabrication to reduce detrimental stress.25 Normally, the stress is highest at the boundary, where the bulk substrate terminates. A high and uneven load on the membrane tends to break it. Compared to the silicon substrate, the membrane is difficult to handle without damaging it, for example, at the phase boundary of liquid and vapor, a common step for any process. Typically, the membrane perforation is left as the final step to avoid further manipulation. Applied to a local area, and without high temperature or corrosive plasma, FIB is the safest choice, especially when the membrane contains other devices from previous fabrication steps.

When using FIB, the main cause for the breaking of membranes arises from the accumulation of charges on such nonconducting surfaces. This is typically visible as a halo around the surrounding conductor in the cutting area. This difference in contrast in the electron micrographs lasts only a couple of seconds and is difficult to capture. Still, it is a precursor of a failure of a cut. The less conducting the surface, the more visible the halo is, and thus more violent the breaking of the membrane. Such halo’s or breakings are not present in membranes completely coated with thin layer of metal. Depending on application, it is possible that repeated low exposure can be used to reduce the local charge accumulation, but in this study, that option was not being studied, as our method was considered faster and more reliable for our application.

Perforating the SiN membrane using FIB offers two major advantages. First, milling with the ion beam is a physical process. It sputters any materials, being metal, oxide, nitride, carbide, or composite. This is an important feature, as the ion beam can be used at any step and has no chemical selectivity requirement. Etching inert materials of the membrane requires corrosive etchants or reactive plasma. This places a huge constraint on the fabrication compatibility point of view. In comparison, the only requirement for FIB processing is vacuum compatibility. Second, FIB is a maskless process that directly sputters material according to a customized design. This is especially fit for research where in house experience is highly important. A wide range of shapes and geometries can be milled directly, and the system is not limited to squares. These two features enable the FIB approach to be employed on a membrane of virtually any material and thickness, as well as any shape and size.

FIB can be used in two milling modes, serial and parallel. Experiments done in this work were performed with serial milling due to its easier use in the first experiments. The advantage with parallel milling is that the whole pattern can be drawn at once, which might help in avoiding the stress reduction phase. If all patterns are being perforated simultaneously on the whole nitride window scale, that might avoid the stress build-up in any location. The remaining problems in the parallel milling are the possible drift during long milling sessions which could dislocate the milling patterns and the limited resolution. Comparing the two milling modes could be a possible future experiment.

VI. SUMMARY

We reported a method to perforate a dielectric membrane using focused gallium beam. The charging due to the ion beam is neutralized with 30 nm thick Cu structures in the
vicinity of the area to be perforated. We found that a square box surrounding the cutting area yields the best result. Furthermore, cutting within a distance of 10 μm from the metal wire connected to charge depletion area allows successful milling of the membrane. With this approach, we were able to perforate a whole membrane with 20 μm wide thin legs on the device platform. Our approach is compatible with a wide range of processes in nanofabrication.

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