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Tunnel-Junction Thermometry Down to Millikelvin Temperatures

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We present a simple on-chip electronic thermometer with the potential to operate down to 1 mK. It is based on transport through a single normal-metal–superconductor tunnel junction with rapidly widening leads. The current through the junction is determined by the temperature of the normal electrode that is efficiently thermalized to the phonon bath, and it is virtually insensitive to the temperature of the superconductor, even when the latter is relatively far from equilibrium. We demonstrate here the operation of the device down to 7 mK and present a systematic thermal analysis.

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I. INTRODUCTION

On-chip electronic thermometry is an important part of modern research and commercial applications of nanotechnology, and it has been studied already for several decades; see Ref. [1] and references therein. Many of these thermometers are based on tunnel junctions or quantum dots [2–4]. Temperature sensors based on normal (N) and superconducting (S) metal tunnel junctions are used in a wide range of experiments [5–7] and applications [8,9]. An example of such a device is a primary Coulomb blockade thermometer (CBT) that is based on normal-metal tunnel junctions with an insulator “I” as a tunnel barrier [normal-metal–insulator–normal-metal (N–I–N)] [10,11], where the electronic temperature can be obtained by measuring the smearing of the single-electron blockade. One more example is a S–N–S thermometer [12], whose critical current $I_c$ depends strongly on the temperature. Primary electronic thermometry has also been successfully demonstrated down to 10 mK using the shot noise of a tunnel junction (SNT) [13–15]. Nowadays, a standard dilution refrigerator reaches a temperature of 5–10 mK, with a record of 1.75 mK [16,17]. Nevertheless, a thermometer that has a modest structure and a simple but accurate temperature reading at sub-10-mK temperatures and does not require a complicated experimental setup is still missing. For this purpose, we present a normal-metal–insulator–superconductor (N–I–S) junction that is widely used both as a refrigerating element and a probe of the local electronic temperature in different experiments and applications [5–9,18–20]. The possibility to use the N–I–S junction at sub-10-mK temperatures makes this thermometer suited for cryogenic applications at low temperatures. For instance, quantum information is a highly focused and rapidly developing field in modern physics. For many realizations, such as superconducting and quantum-dot qubits, one needs to define a set of quantum states at low temperature, that are well separated and well controlled and insensitive to noise and decoherence effects [21]. Several experimental realizations of two-level systems [22–24] suggest that decreasing temperature further will increase the coherence times as well as improve charge sensitivity. We think that our thermometer will be interesting for a community who is willing to discover new physics as well as improve already existing devices that require low temperatures for their proper functioning. The N–I–S thermometer is easy to operate compared to SNT [13,14], and its thermalization is quite straightforward compared to CBT [11] due to the single-junction configuration and can be combined on chip with other solid-state devices. A measurement of the N–I–S current–voltage ($I–V$) characteristic yields a primary temperature reading.

In this paper, we study both experimentally and theoretically an on-chip electronic thermometer based on a single N–I–S tunnel junction at sub-10-mK temperatures. We demonstrate the operation of the N–I–S thermometer down to 7.3 mK. In addition, we develop a thermal model that explains our measurement data and shows that self-heating effects remain negligible for temperatures down to 1 mK.

II. THEORETICAL BACKGROUND

Transport through a N–I–S junction has strong bias and temperature dependence. Near zero bias voltage, the current is suppressed due to the superconducting gap, $\Delta$ [25]. When biased at voltage $V$, the current depends on the...
temperature due to the broadening of the Fermi distribution $f_N(E) = (e^{E/k_BT_N} + 1)^{-1}$ in the normal metal with the temperature $T_N$ and the Boltzmann constant $k_B$. The current can be expressed as [25]

$$\begin{align*}
I &= \frac{1}{2eR_T} \int_{-\infty}^{+\infty} dE n_s(E) [f_N(E-eV) - f_N(E+eV)], \\
&= \frac{1}{2eR_T} \int_{-\infty}^{+\infty} dE n_s(E) [f_N(E-eV) - f_N(E+eV)]
\end{align*}$$

(1)

where $R_T$ is the tunneling resistance of the junction, and $E$ is the energy relative to the chemical potential.

In the superconductor, the Bardeen-Cooper-Schrieffer density of states is smeared and typically described by the Dynes parameter $\gamma$ expressed as $n_s(E) = |\text{Re}(u/\sqrt{u^2-1})|$ (see the Supplemental Material in Ref. [26]), where $u = E/\Delta(T_S) + i\gamma$, and $T_S$ is the temperature of the superconductor. Possible origins of $\gamma$ include broadening of the quasiparticle energy levels due to finite lifetime [27], Andreev current [28,29], as well as photon-assisted tunneling caused by high-frequency noise and black-body radiation [26]. The typical experimental range of $\gamma$ for Al-based [30] tunnel junctions is $10^{-4}$ to $10^{-5}$ for a single $N-I-S$ junction [26,35], getting as low as $10^{-7}$ in $S-I-N-I-S$ single-electron transistors with multistage shielding [36].

One can determine $T_N$ from a measured $I$-$V$ curve using Eq. (1). As we show below, the self-heating of both $N$ and $S$ electrodes has a small effect on the $I$-$V$ characteristic; thus, for now, we neglect these effects. Therefore, we assume temperatures to be small, $k_BT_{NS} \ll eV, \Delta(T_S)$, and the superconducting gap to be constant and equal to its zero-temperature value $\Delta$. In this case, for $eV < \Delta$, one can approximate Eq. (1) by

$$I = I_0 e^{-(\Delta-eV)/k_BT_N} + \frac{\gamma V}{R_T \sqrt{1 - (eV/\Delta)^2}},$$

(2)

where $I_0 = \sqrt{2\pi\Delta k_BT_s/2eR_T}$ [6,28]. Here the second term stands for the corrections to the $I$-$V$ characteristic due to smearing. It leads to the saturation of the exponential increase of the current at low bias values. In the regime of moderate bias voltages, one can neglect this term and invert Eq. (2) into

$$V = \frac{\Delta}{e} + \frac{k_BT_N}{e} \ln(I/I_0).$$

(3)

This equation provides a way to obtain the electronic temperature $T_N$ by only the fundamental constants and by the slope of the measured $I$-$V$ characteristic on a semilogarithmic scale as

$$T_N^R(V) = \frac{e}{k_B} \frac{dV}{d(\ln I)}.$$  

(4)

This Eq. (4) allows us to use the $N-I-S$ junction as a primary thermometer, however, with some limitations. One can include the effects of $\gamma$ into Eq. (4) by subtracting the last term in Eq. (2) from the current $I$ and obtain better accuracy. We do not take this approach here, since the main advantage of Eq. (4) is its simplicity as a primary thermometer without any fitting parameters.

Next, we will compare the two methods used to extract the electronic temperature from the measured $I$-$V$ curves. In method $A$, we employ Eq. (1) and perform a nonlinear least-squares fit of a full $I$-$V$ characteristic with $T_N$ as the only free parameter. The value of $T_N$ obtained in this manner, named $T_N^A$, is not sensitive to $\gamma$. Method $B$ is based on the local slope of the $I$-$V$ [see Eq. (4)]. The smearing parameter $\gamma$ has an influence on the slope of the $I$-$V$ characteristic and, thus, induces errors in the temperature determination. The temperature $T_N^B$ is extracted as the slope of measured $V$ vs $\ln I$ over a fixed $I$ range for all temperatures where Eq. (4) is valid. In the experiment, it is usually difficult to determine the environment parameters precisely, but one can determine $\gamma$ from the ratio of $R_T$ and the measured zero bias resistance of the junction. The $I$-$V$, which takes the $\gamma$ parameter into account [see Eq. (2)], gives indistinguishable results from the ones obtained by method $A$.

We evaluate the influence of the $\gamma$ parameter on the relative deviations of the present thermometer based on method $B$ numerically, as shown in Fig. 1(a). This deviation $t$ is defined as the relative error $t = (T_N^B/T_N) - 1$. We show the values of $t$ vs $\ln(eR_T I/\Delta)$ for two extreme cases $\gamma = 10^{-7}, 10^{-4}$, and for $\gamma = 2.2 \times 10^{-5}$ extracted from the present experiment at temperatures of 1, 3, and 7 mK.

![Image](034001-2)}
The lowest bath temperature is 3 mK, and 7 mK is the saturation of the electronic temperature in the current experiment. The larger the values of $\gamma$ and the lower the temperature, the higher the relative deviations. In addition, the range of the slope used to extract $T_N^R$ shrinks with increasing $\gamma$ and with decreasing temperature [see, e.g., red curves in Fig. 1(a)]. Thus, reducing the leakage will significantly improve the accuracy of the device, especially towards lower temperatures. Possible avenues for suppressing $\gamma$ include improved shielding [36,37] and encapsulating the device between ground planes intended to reduce the influence of the electromagnetic environment [26]. Finally, higher tunneling resistance of the junction decreases the Andreev current [38]. We note that one can also use $dV/d(\ln g)$ as a primary thermometer, where $g = dl/dV$ is the differential conductance—typically a more precise measurement since it is done with a lock-in technique. Compared to Eq. (4), this method has the minimal deviation $t$ reduced by at least a factor of 3.5 for $T_N \geq 1$ mK (6 for $T_N \geq 10$ mK), though exhibiting qualitatively similar dependencies on $\gamma$ and $T_N$.

III. EXPERIMENTAL REALIZATION AND MEASUREMENT TECHNIQUES

Next, we describe the realization of the $N-I-S$ thermometer that is shown together with a schematic of the experimental setup in the scanning-electron micrograph in Fig. 2. The device is made by electron-beam lithography and the two-angle shadow evaporation technique [39]. The ground plane under the junction is made out of 50 nm of Cu. To electrically isolate the ground plane from the device, we cover the Au layer with 100 nm of AlO$_x$ using atomic-layer deposition. Next, we deposit a layer of $d_N = 40$ nm of Al that is thermally oxidized in situ. The last layer is formed immediately after the oxidation process by deposition of $d_S = 150$ nm of Cu, thus, creating a $N-I-S$ tunnel junction with an area $A = 380 \times 400$ nm$^2$.

The geometry of the junction is chosen such that the leads immediately open up at an angle of $90^\circ$ and create large pads with an area $A_N = A_S = 1.25 \text{ mm}^2$ providing good thermalization. The $S$ lead is covered by a thick normal-metal shadow as shown in brown in the inset of Fig. 2, where the $N$ and $S$ layers are interfaced by the same insulating layer of AlO$_x$ as the junction.

The experiment is performed in a dilution refrigerator (base temperature 9 mK) where each of the sample wires is cooled by its own separate Cu nuclear refrigerator (NR) [40], here providing bath temperatures $T_{bath}$ down to 3 mK. Nuclear refrigerator temperatures after demagnetization are highly reproducible and obtained from the precoupling temperatures and previously determined efficiencies [11]. Temperatures above approximately 9 mK are measured with a cerium magnesium nitrate thermometer which is calibrated against a standard superconducting fixed-point device. Since the sample is sensitive to the stray magnetic field of that applied on the nuclear refrigerator, this field is compensated down to below 1 G using a separate solenoid. The $I$-$V$ curves (see Fig. 2 for the electrical circuit) are measured using a home-built current preamplifier with input offset-voltage stabilization [41] to minimize distortions in the $I$-$V$ curves.

Filtering, radiation shielding, and thermalization are crucial for obtaining a low $\gamma$ and low device temperatures. Each sample wire goes through 1.6 m of thermocoax, followed by a silver epoxy microwave filter [42], a 30-kHz low-pass filter, and a sintered silver heat exchanger in the mixing chamber before passing the Al heat switch and entering the Cu nuclear stage. The setup is described in detail in Ref. [11] and is further improved here (see the Appendix for more details).

IV. RESULTS AND DISCUSSION

In Fig. 1(b), the measured $I$-$V$ characteristic in the superconducting gap region is shown by blue dots. The solid red line corresponds to the full fit based on method $A$. In the inset, we present the $I$-$V$ characteristic at a larger voltage scale used to extract $R_T$. In Fig. 3(a), the measured $I$-$V$ characteristics of the $N-I-S$ junction are shown in logarithmic scale by blue dots at $T_{bath} = 100, ..., 3$ mK from left to right. The full fits are shown as dashed red lines. The tunneling resistance $R_T = 7.7$ k$\Omega$ and the Dynes parameter $\gamma = 2.2 \times 10^{-5}$ used in all these fits are determined based on the $I$-$V$ characteristics shown in Fig. 1(b) at high and low voltages, respectively. For the lowest temperatures, $T_N$ from the nonlinear fit depends strongly on the superconducting gap [43], making it difficult to determine the gap with high enough accuracy [44]. However, Eq. (1) gives a possibility to perform a nonlinear least-squares fit, and Eq. (3) gives a linear fit, where the parameters $\Delta$ and $T_N$ are responsible for the offset and the slope, respectively. Therefore, at high temperatures (approximately 100 mK in the present experiment), one can narrow down the uncertainty in $\Delta$ such that $T_N$ becomes essentially

FIG. 2. A scanning-electron micrograph of the $N-I-S$ device together with a schematic of the experimental setup. In the main panel, the $S$ and $N$ leads of the junction are visible, and underneath the pads, the ground plane of a square shape is indicated by a dashed line. Enlarged inset shows the actual tunnel junction where the $S$ and $N$ leads are shown in blue and brown, respectively.
The N-I-S temperature decreases slowly over time, arriving at 7.3 mK several weeks after the cooldown from room temperature. This suggests that internal relaxation causing a time-dependent heat leak, e.g., in the silver epoxy sample holder, is limiting the minimum temperature. Future improvements will employ low-heat-release materials better suited for ultralow temperatures such as sapphire or pure annealed metals, e.g., for the socket and chip carrier, minimizing organic noncrystalline substances such as epoxies.

V. THERMAL MODEL

The total power dissipated in the device is equal to

$$IV = \dot{Q}_{N-I-S}^N + \dot{Q}_{N-I-S}^S,$$

where $\dot{Q}_{N-I-S}^N$ and $\dot{Q}_{N-I-S}^S$ are the heat powers to the normal metal and to the superconductor, respectively. The heat released to the superconductor is given by

$$\dot{Q}_{N-I-S}^S = \frac{1}{e^2R_T} \int E_N n_s(E)[f_N(E-eV)-f_S(E)]dE, \quad (5)$$

where $E_S = E$ is the quasiparticle energy. To evaluate $\dot{Q}_{N-I-S}^N$, one has to substitute $E_S$ by $E_N = (eV - E)$ in Eq. (5). Almost all of the heat is delivered to the superconductor in the measured (subgap) bias range, thus, $\dot{Q}_{N-I-S}^S \sim IV$ and $\dot{Q}_{N-I-S}^N \ll \dot{Q}_{N-I-S}^S$.

So far, we neglect all self-heating effects both in the normal metal and in the superconductor. To justify the non-self-heating assumption, we check numerically and analytically these self-heating effects. We sketch the analytical arguments in the Appendix. Here we state the main results obtained from the thermal model.

Self-heating of the superconductor can take place due to the exponential suppression of thermal conductivity and the weak electron-phonon (e-ph) coupling, especially at low temperatures. We find that the superconductor temperature $T_S$ stays below 250 mK in the subgap bias range $|V| \leq (\Delta/e)$ and does not influence the thermometer reading. In this bias range and at $T_{bath} = 3$ mK, we estimate based on the numerical calculations the temperature of the superconductor $T_S = 145$ mK and the power injected to the superconductor is $IV \sim 90$ fW. At the same time, we evaluate the relative change of the slope to be small $|\Delta| \lesssim 5 \times 10^{-3}$ at $I \lesssim 1$ nA. In conclusion, the temperatures obtained from both methods $A$ and $B$ are affected by less than 0.5% by self-heating of the superconductor [45]. In addition, the normal metal might get self-heated by less than 0.5% by self-heating of the superconductor [46]. The influence of the self-heating of the normal metal down to 1 mK temperature affects the temperature obtained from both methods $A$ and $B$ by less than 0.5% as well, as in the case of self-heating of the superconductor.

FIG. 3. Panel (a) shows measured $I$-$V$ characteristics (blue dots), when $T_{bath}$ is lowered from left to right together with fits as solid black and dashed red lines (see text). (Inset) Close-up of two $I$-$V$ characteristics for temperatures of 10 and 7 mK. The electronic temperatures extracted from both the full fit (red squares) and method $A$ (black triangles) vs $T_{bath}$ are shown in (b).

The lowest temperature obtained from the full fit is $T_N^S = 7.3$ mK with statistical uncertainty of 5% at $T_{bath} = 3$ mK.
VI. CONCLUSIONS

In conclusion, we demonstrate experimentally the operation of an electronic thermometer based on a single $N$-$I$-$S$ tunnel junction. The thermometer agrees well with the refrigerator thermometer down to about 10 mK and reaches a lowest temperature of 7.3 mK at $T_{\text{bath}} = 3$ mK, currently limited by a time-dependent heat leak to the sample stage. We discuss several possible improvements of the present device and experimental setup. Finally, we show that self-heating in the normal metal and in the superconductor on the full $I$-$V$ or its slope is negligible, paving the way for $N$-$I$-$S$ thermometry down to 1 mK if the experimental challenges can be overcome.

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A. V. F. and L. C. contributed equally to this work.

APPENDIX

1. Experimental techniques

The setup described in Ref. [11] is improved as follows. First, a ceramic chip carrier is replaced by silver epoxy parts which remain metallic to the lowest temperatures, allowing more efficient cooling. Further, the sample—previously mounted openly inside the cold-plate radiation shield together with the nuclear stages—is enclosed in an additional silver shield, sealed with silver paint against the silver epoxy socket, and thermalized to one of the Cu refrigerators (see Fig. 4). Finally, each wire is fed into the sample shield through an additional silver epoxy microwave filter. While previously saturating at 10 mK or above [11], metallic CBTs have given temperatures around 7 mK after the improvements [42,47], comparable to the $N$-$I$-$S$ temperatures presented here.

2. Estimates of the subgap conductance

The Dynes parameter $\gamma$ discussed in the main text can be attributed to the higher-order processes such as Andreev tunneling events. Assuming ballistic transport and an effective area of conduction channel $A_{\text{ch}} = 30$ nm$^2$

\[
\frac{\gamma}{\text{mK}} \approx \frac{10^{-4}}{30 \text{nm}^2} = 3.3 \times 10^{-7}
\]

\[
\frac{\gamma}{\text{mK}} \approx \frac{10^{-4}}{30 \text{nm}^2} = 3.3 \times 10^{-7}
\]

\[
\sigma_{\text{AR}} = \frac{R_K}{8N R_T} = 8.5 \times 10^{-5}
\]

where $R_K$ is the quantum resistance, $N$ is the effective number of the conduction channels, and $N = A/A_{\text{ch}}$, where

$\text{FIG. 4. Scheme of a dilution unit together with a NR. Radiation}
\text{shields (not shown) are attached to the still and cold plate (approximately 50 mK). The resistor-capacitor (RC) filters are 1.6 k\Omega/2.2 nF and 2.4 k\Omega/470 pF. The 21 NR plates are 0.25 \times 3.2 \times 9.0 \text{ cm}^3 each, amounting to 64-g Cu per plate. The NRs cool as low as 0.2 mK. In the present experiment, the lowest T_{\text{bath}} used is 3 mK. Compared to Ref. [11], the improved setup depicted here features a Ag epoxy socket, a Ag epoxy chip carrier, and a second filtering stage with radiation-tight feedthroughs into an additional sample radiation shield. The abbreviations BW, B, and GND presented in the schematic stand for bandwidth, critical magnetic field and electrical ground, respectively.}$

\[034001-5\]
A is the area of the junction. Alternatively, the estimate based on diffusively enhanced Andreev conductance yields of corresponding dimensionless conductance is \(7.5 \times 10^{-5}\). These values are of the same order of magnitude as in our experiment \((\gamma = 2.2 \times 10^{-5})\) and fall in the range of earlier experiments [29].

3. Theoretical estimates for the relative deviations of the present thermometer

The theoretical deviations of \(dV/d(I)\) at \(\gamma = 2.2 \times 10^{-5}\) numerically calculated from \(J-V\), Eq. (1), are rather large, particularly at low temperatures [approximately 30% at 1 mK; see solid blue curves in Figs. 1(a) and 5]. Measuring the differential conductance \(g = dI/dV\) rather than current \(I\) significantly reduces the predicted deviations \(t_g = T^\text{slo}_{\text{pe},g}/T_N - 1\), where \(T^\text{slo}_{\text{pe},g} = [dV/d(I)]e/k_B\). The minimum of these deviations gets broader and potentially reduces measurement noise since it is a lock-in measurement—overall strengthening method B. In Fig. 5, for comparison we show two sets of curves for \(t_g\) (thick purple curves) and \(t\) (thin blue curves) from left to right. Sets are calculated based on the experimental parameters for \(\gamma = 2.2 \times 10^{-5}\), \(\Delta = 200 \mu\text{eV}\), and \(R_T = 7.7 \text{k}\Omega\). Each set corresponds to the temperatures 1, 3, and 7 mK and is shown as dash-dotted, dashed, and solid lines, respectively. Here, the \(t\) set is identical to the set with \(\gamma = 2.2 \times 10^{-5}\) that is shown in Fig. 1(a).

4. Self-heating of the superconductor

First, we consider the self-heating of the superconductor. We study the heat transport in the present geometry (see Fig. 2) by a diffusion equation assuming thermal quasiparticle energy distribution [50,51]

\[-\nabla(k_S\nabla T_S) = u_S,\quad (A1)\]

where we set a boundary condition near the junction \(-\hat{n}_\text{inner}k_S\nabla T_S|_{\text{junct}} = \dot{Q}_{\text{N-I-S}}/A\), where \(\hat{n}_\text{inner}\) is the inner normal to the junction. Thermal conductivity in the superconductor is

\[k_S = \frac{6}{\pi^2} \left( \frac{\Delta}{k_BT_S} \right)^2 e^{-\frac{\Delta}{2k_BT_S}} L_0 T_S \sigma_{A1},\quad (A2)\]

where \(L_0\) is the Lorenz number, and \(\sigma_{A1} = 3 \times 10^{17} (\Omega\text{m})^{-1}\) is the electrical conductivity of the Al film in the normal state [51], and we take into account the \(T_S\) dependence of the gap at low temperatures, \(\Delta(T_S)/\Delta = 1 - \sqrt{2\pi k_BT_S/\Delta} e^{-\Delta/k_BT_S}\). The absorbed heat is given by \(u_S = \dot{q}^S_{\text{e-ph}} + \dot{q}_{\text{trap}}\). Here, the first term is the electron-phonon power \(\dot{q}^S_{\text{e-ph}} = \Sigma_{A1}(T_S^5 - T_p^5) \exp(-\Delta/k_BT_S)\) [52], where \(\Sigma_{A1} = 3 \times 10^8 \text{W K}^{-5} \text{m}^{-3}\) is the material-dependent electron-phonon coupling constant. The phonon temperature \(T_p\) is assumed to be equal to \(T_{\text{bath}}\). Because of weak electron-phonon coupling, nearly all the heat is released through the (unbiased) normal-metal shadow (see Fig. 6) that acts as a trap for quasiparticles, \(\dot{q}_{\text{trap}}\). Here the conductance of the trap per unit area is the same as in [29].

The conductance of the trap per unit area is the same as for the tunnel junction \(\sigma_T = 1/(R_T A)\). Therefore, the heat removed per volume by this trap \(\dot{q}_{\text{trap}}\) can be calculated using Eq. (5) at \(V = 0\), \(T_N = T_{\text{bath}}\), and substituting \(R_T\) by \(d_S/\sigma_T\). Thus, the temperature of the superconductor \(T_S\) can be found as the solution of Eq. (A1) in 2D in polar coordinates using radial approximation for the sample geometry and can be written as [51]

\[\sqrt{2\pi k_BT_S/\Delta(T_S)} e^{-\Delta(T_S)/k_BT_S} \equiv \alpha Q^S_{\text{N-I-S}}.\quad (A3)\]

Here, we indeed assume \(Q^S_{\text{N-I-S}} \sim IV\), and \(\alpha = \sqrt{\pi e^2 G}/[d_S\sigma_{A1}\sqrt{2\pi k_BT_S \Delta(T_S)}]\) is a coefficient that depends on \(T_S\), and dimensionless parameter \(G = [\ln(\lambda r_0)/\theta] \sim 2, \ldots, 3\) is logarithmically dependent on the sample geometry [51]. Here, \(\lambda\) is the relaxation length of the order of approximately 10, . . . , 100 \(\mu\text{m}\), and \(r_0 = 2A/(\pi d_N) \approx 500 \text{ nm}\) is

![FIG. 5. The theoretical deviations \(t\) and \(t_g\) of the thermometer reading using method B based on \(I(V)\) and \(g(V)\), respectively. These deviations are shown for temperatures 1, 3, and 7 mK as dash-dotted, dashed, and solid lines, respectively. Parameters used are \(\gamma = 2.2 \times 10^{-5}\), \(\Delta = 200 \mu\text{eV}\), and \(R_T = 7.7 \text{k}\Omega\) as in the actual experiment.](image)

![FIG. 6. The thermal diagram of the N-I-S thermometer. Present schematic does not reflect the real thicknesses of the materials. In this thermal model, we assume the normal-metal shadow that acts as the trap to be at \(T_{\text{bath}}\).](image)
the radius of the contact in the present device. After substitution of all the parameters, we find that the superconductor temperature $T_S$ does not influence the thermometer reading staying below 250 mK in the subgap bias range $|V| \lesssim (\Delta / e)$. We estimate $T_e$ to be approximately 145 mK in this bias range at $T_{bath} = 3$ mK corresponding to the power injected to the superconductor as $IV \sim 90$ fW, and the quasiparticle density [51] as $n_{qp} = 0.3 \mu m^{-3}$. In addition, we evaluate the relative change of the slope to be small $|r| \lesssim 5 \times 10^{-3}$ at $I \lesssim 1$ nA. In conclusion, the temperatures obtained from both methods $A$ and $B$ are affected by less than 0.5% by self-heating of the superconductor.

5. Self-heating of the normal metal

For self-heating of the normal metal, one can solve the diffusion equation Eq. (A1) taking into account the same boundary condition as above with all $S$ indices replaced by $N$, where $\kappa_N = L_0\sigma_{Cu}T_N$ is the thermal conductivity of the normal metal, and the electrical conductivity of Cu is assumed to be $\sigma_{Cu} = 5 \times 10^7 (\Omega m)^{-1}$ [53]. The heat absorbed in the normal metal is $u_N = \dot{q}_{N-\text{ph}}^N + \dot{q}_{N-\text{wire}}^N$. The heat conduction through the gold bonding wires $\dot{q}_{N-\text{wire}}^N$ is taken into account only at the point where it is attached to the normal-metal pad, whereas the electron-phonon interaction $\dot{q}_{N-\text{ph}}^N$ is effective in the full volume of the normal metal. The volumetric electron-phonon power is $\dot{q}_{N-\text{ph}}^N = \sum_N (T_N^5 - T_p^5)$, where $\sum_N = 2 \times 10^9 \text{W K}^{-5} \text{m}^{-3}$ is the electron-phonon coupling constant of copper. Here we consider the effect of the heat removed by the bonding wires on the temperature only in the normal metal, thus, $\dot{q}_{N-\text{wire}}^N = L_0\sigma_{Au}(T_N^2 - T_{bath}^2)/2L_{wire}d_N$. The length of the gold bonding wire is $L_{wire} \approx 5$ mm and $\sigma_{Au} = 1.8 \times 10^9 (\Omega m)^{-1}$ is the electrical conductivity of gold measured at low temperatures. The thermal relaxation length in the normal metal [1] is

$$l_N = (T_{bath}^{p/2-1})^{-1}\sqrt{(\sigma_{Cu}L_0)/(2\Sigma_{Cu})}, \quad (A4)$$

We substitute $p = 5$ and $T_{bath} = 10$ mK and obtain $l_N = 17.5$ mm. Since all the dimensions of the present device are smaller than 1.5 mm, there is only a weak temperature gradient over the normal-metal electrode due to its good heat conduction, and the electron-phonon coupling weakens at low temperatures. By solving the heat-balance equation $\dot{Q}_{N-\text{ph}}^N = \dot{Q}_{N-\text{ph}}^N + \dot{Q}_{N-\text{wire}}^N$ and assuming no external heat leaks, one can calculate $T_N$. Here, the heat released through the $e$-ph coupling is $\dot{Q}_{N-\text{ph}}^N = \Omega_N \dot{q}_{N-\text{ph}}^N$, where $\Omega_N = A_Nd_N$ is the volume of the $N$ electrode. The heat released through $N_{wire} = 2$ bonding wires is $\dot{Q}_{N-\text{wire}}^N = \dot{q}_{N-\text{wire}}^N A_{wire}d_N$, where its cross-sectional area is $A_{wire} = \pi r_{wire}^2$ with a radius $r_{wire} = 16 \mu m$. The temperature obtained from both methods $A$ and $B$ is affected by less than 0.5% by self-heating of the normal metal down to a temperature of 1 mK. In addition, we can evaluate at low temperatures $T_N \lesssim T_S \ll \Delta/k_B$ the maximum cooling at optimum bias voltage $V_{opt} \approx (\Delta - 0.66k_BT_N)/e$ [1],

$$\dot{Q}_{N-\text{ph}}^N \approx \frac{\Delta^2}{e^2RT_N} \left[-0.59 \left(\frac{k_BT_N}{\Delta}\right)^{3/2} + \frac{2k_BT_N}{\Delta} \times \exp \left(-\frac{\Delta}{k_BT_N} + \gamma\right)\right]$$

(A5)

to be 90 pW at $T_{bath} = 1$ mK.


[30] Tunnel junctions based on Nb, NbN, or NbTiN have higher $\gamma$ values, usually up to $10^{-2}$ [31–34].


[43] A reduction of the superconducting gap by 0.1% changes $T_N$ by 10% at the lowest temperature.

[44] With the given experimental uncertainties, we can determine the gap with a precision of 0.25% ($\pm 0.5 \mu$eV).

[45] The temperature of the superconductor will affect the $T_N$ through the dependence of the I-V curve on the magnitude of the gap. The geometry of the device can influence the number of quasiparticles and, consequently, the effective temperature of the superconductor.